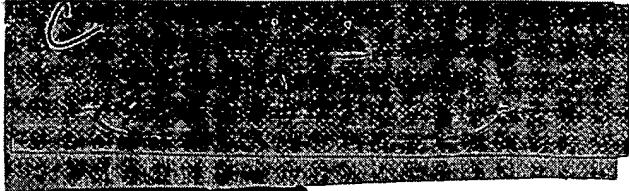


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09/387109

09/31/99

Class	Subclass
ISSUE CLASSIFICATION	



PATENT NUMBER

U.S. UTILITY PATENT APPLICATION

O.I.P.E. SCANNED <i>[Signature]</i> O.A. <i>[Signature]</i>	PATENT DATE <i>[Blank]</i>
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APPLICATION NO. 09/387109	CONT/PRIOR	CLASS 212 711	SUBCLASS 418	ART UNIT 2183 256	EXAMINER <i>[Signature]</i>
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SDRAM CONTROLLER FOR PARALLEL PROCESSOR ARCHITECTURE

TITLE

PTO-2040
12/99

PREPARED AND APPROVED FOR ISSUE

ISSUING CLASSIFICATION

ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
INTERNATIONAL CLASSIFICATION							

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<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> a) The term of this patent subsequent to _____ (date) has been disclaimed. <input type="checkbox"/> b) The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Assistant Examiner) _____ (Date)			NOTICE OF ALLOWANCE MAILED	
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<input type="checkbox"/> c) The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) _____ (Date)			ISSUE BATCH NUMBER	

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